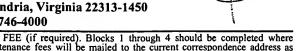
PART B - FEE(S) TRANSMITTAL

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02/13/2004

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(Depositor's name)	Ackerman	B	Stephan	-
(Signature)		1	THE STATE OF THE S	_ (
(Date)	4, 2004	24	March	
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APPLICATION NO. FILING DATE FIRST NAMED INVENTOR ATTORNEY DOCKET NO. CONFIRMATION NO. 10/664,262 09/17/2003 Yong Meng Lee CS01-211

TITLE OF INVENTION: METHOD OF FORMING DOUBLE-GATED SILICON-ON-INSULATOR (SOI) TRANSISTORS WITH REDUCED GATE TO SOURCE-DRAIN OVERLAP CAPACITANCE

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1330	\$300	\$1630	05/13/2004
EXAMINER		ART UNIT	CLASS-SUBCLASS]	
ISAAC, STANETTA D		2812	438-151000	•.	

1. Change of correspondence address or indication of "Fee Address" (37

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(A) NAME OF ASSIGNEE Semiconductor Chartered

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

Singapore

Please check the appropriate assignee category or categories (will not be printed on the patent);

☐ individual — corporation or other private group entity

4a. The following fee(s) are enclosed:

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